



Serial No.: 10/074,648  
Title: SYSTEM & METHOD FOR ADDRESSING OPTICAL EMANATIONS  
FROM AN INFORMATION PROCESSING DEVICE  
Inventor(s): Loughry  
Docket No.: 41992-00541  
James L. Johnson  
(701) 293-7680

1/20

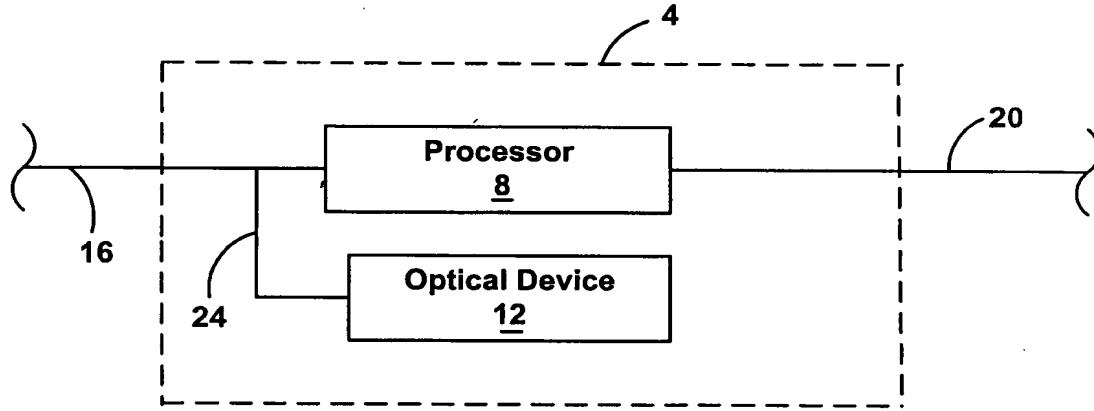


FIG. 1A

(Prior Art)

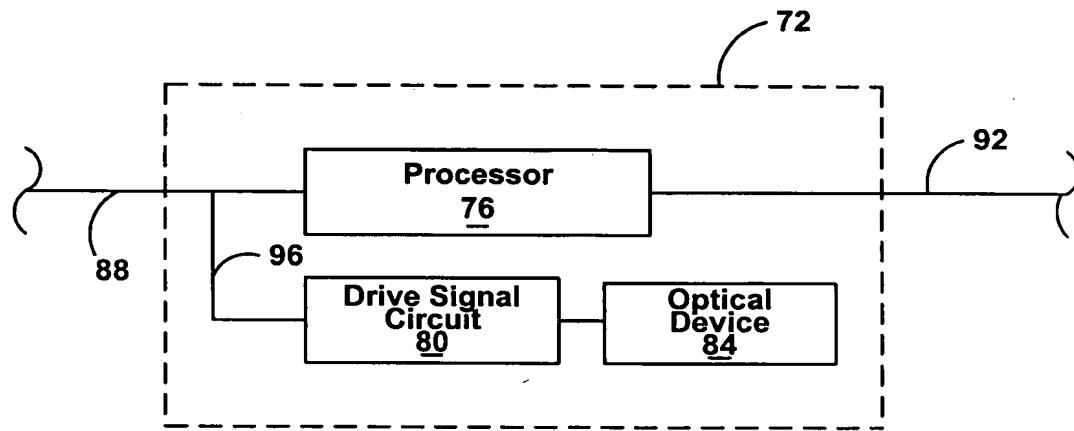


FIG. 4

2/20

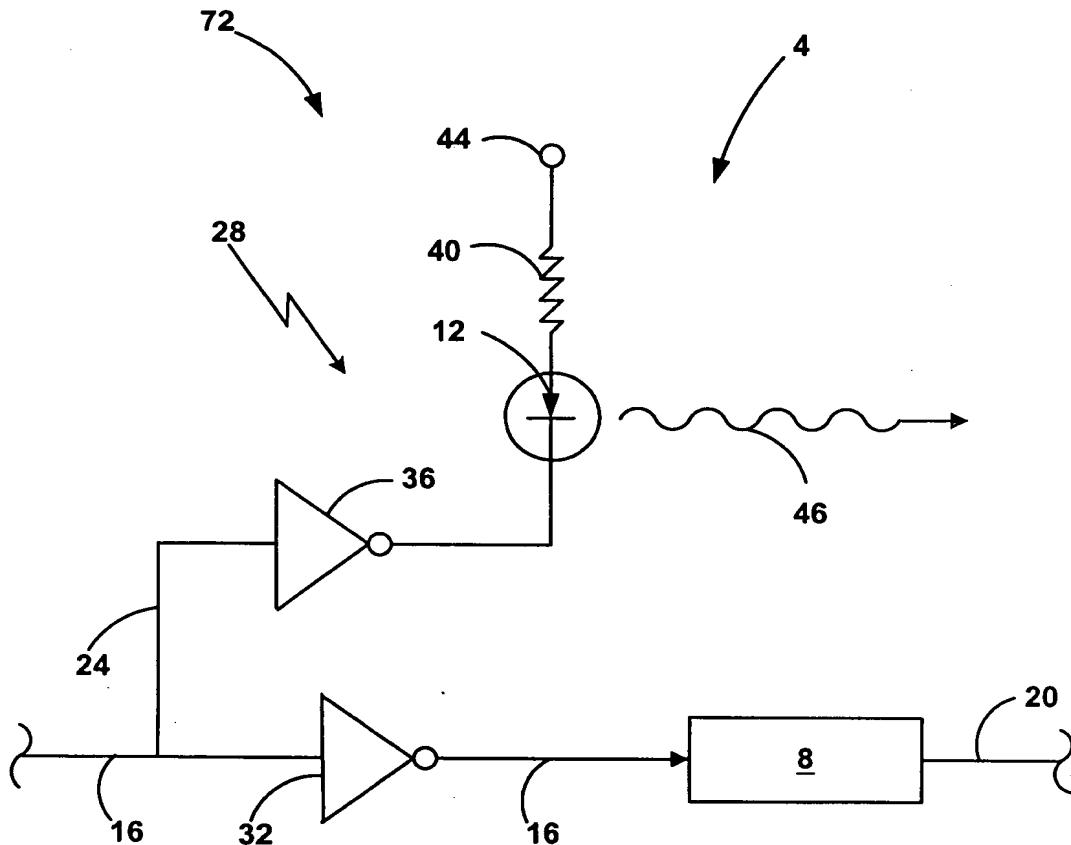


FIG.1B

(Prior Art)

3/20

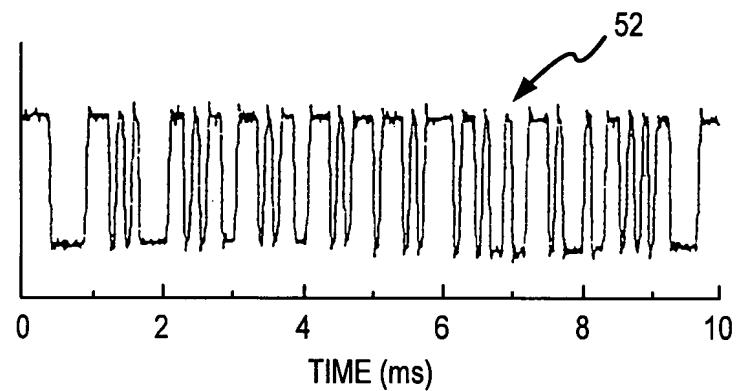


FIG.2B

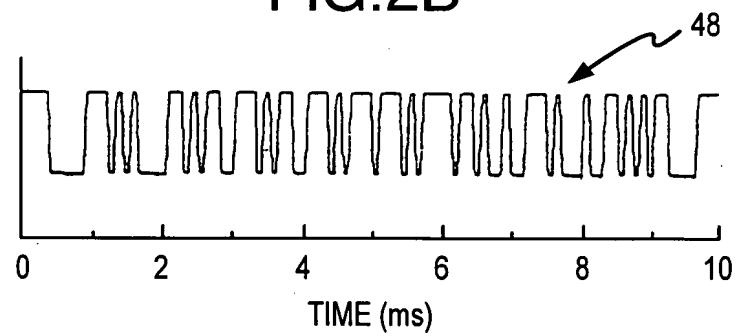


FIG.2A

4/20

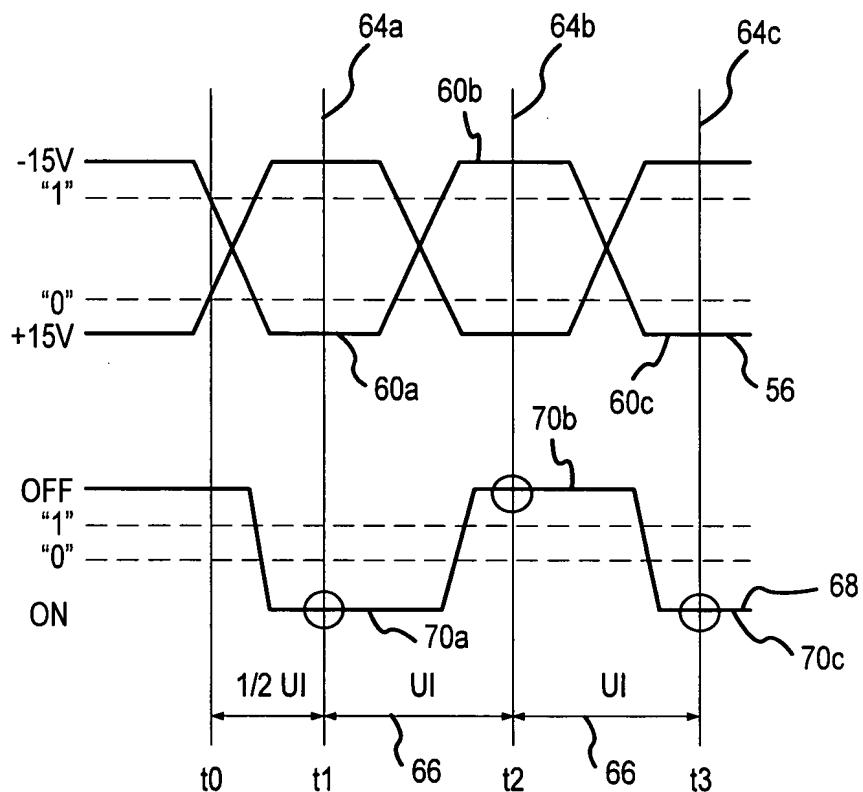


FIG.3

5/20

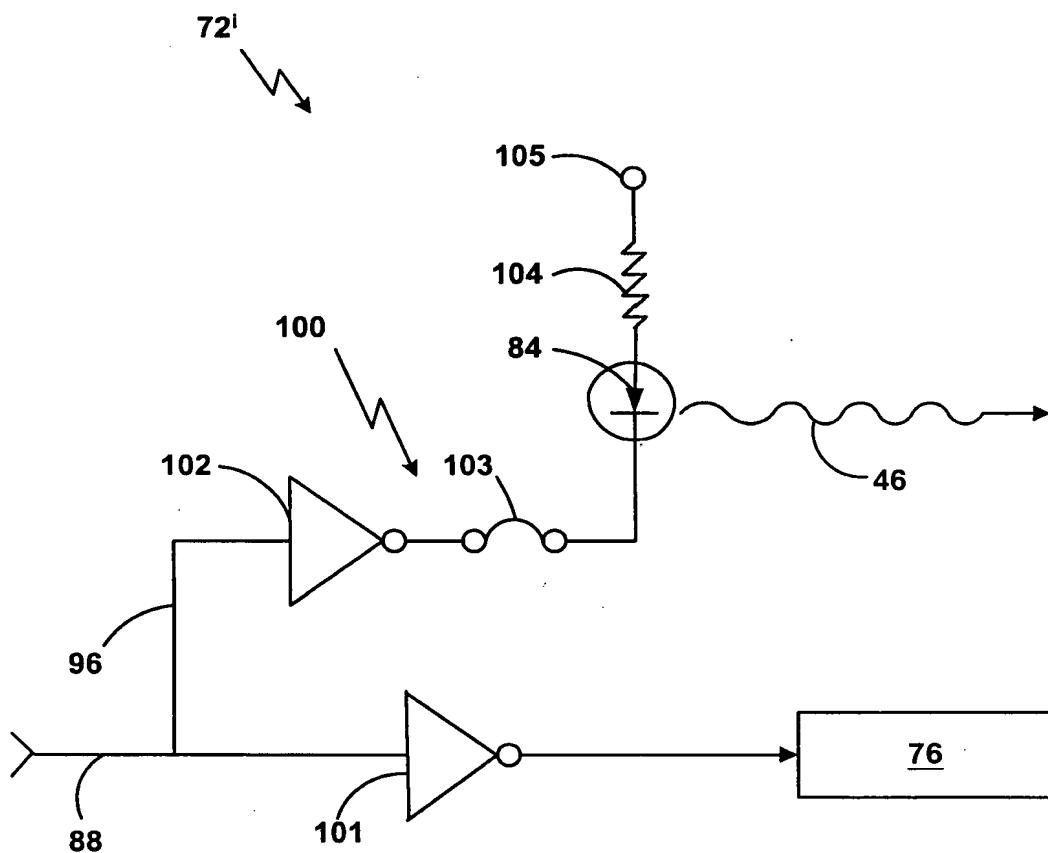


FIG.5

6/20

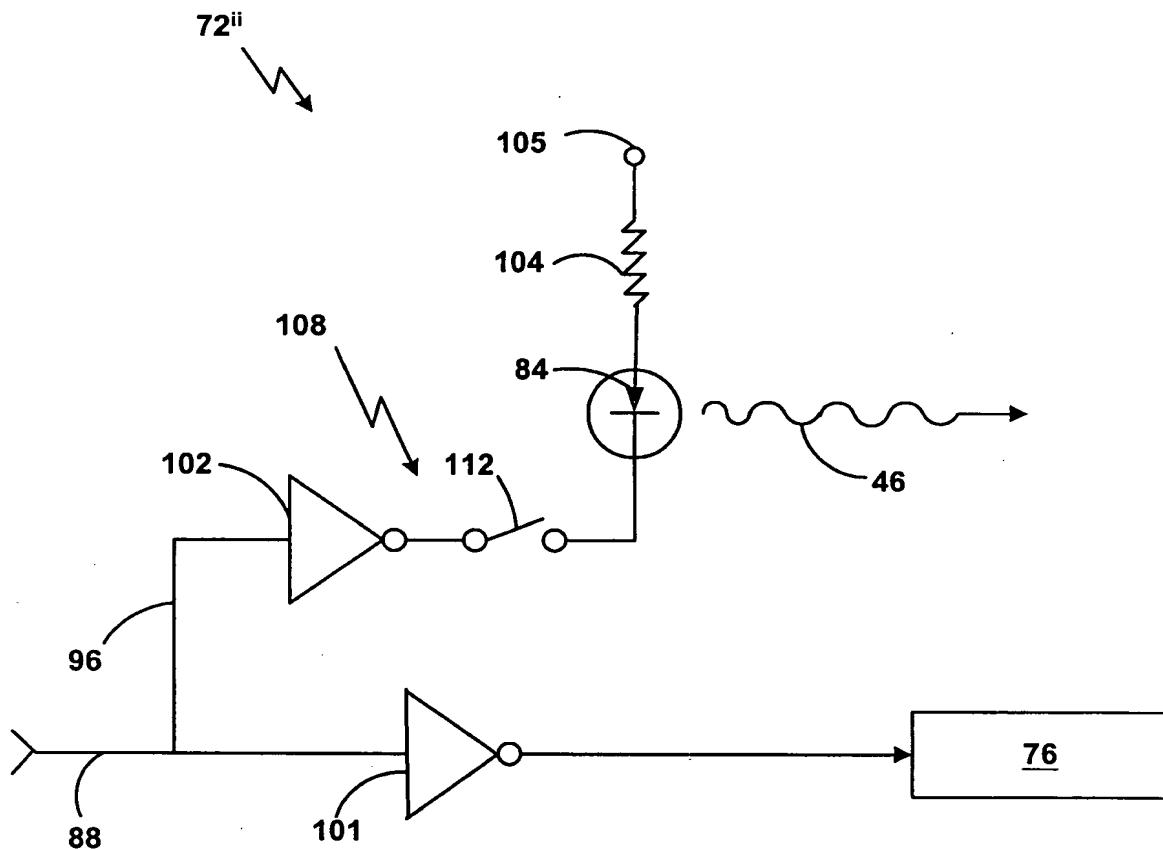


FIG.6

7/20

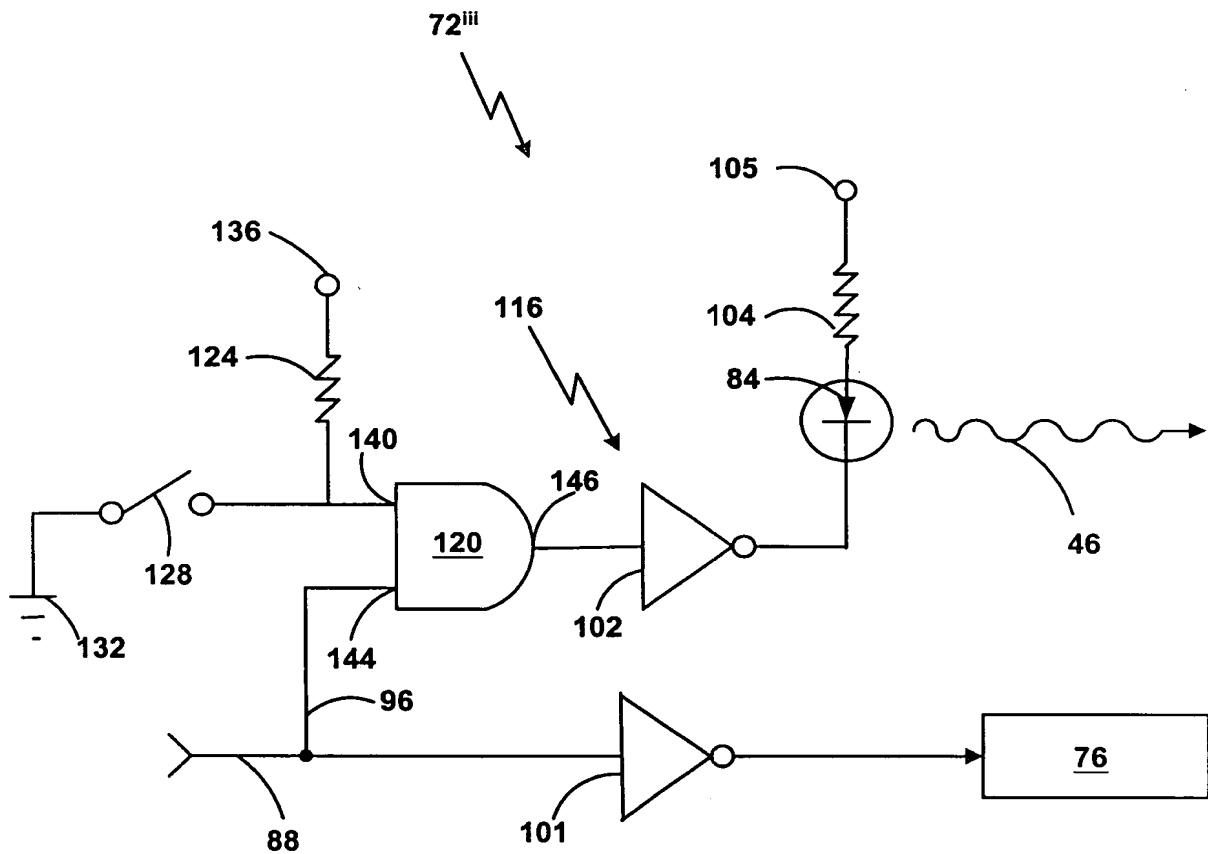


FIG.7

8/20

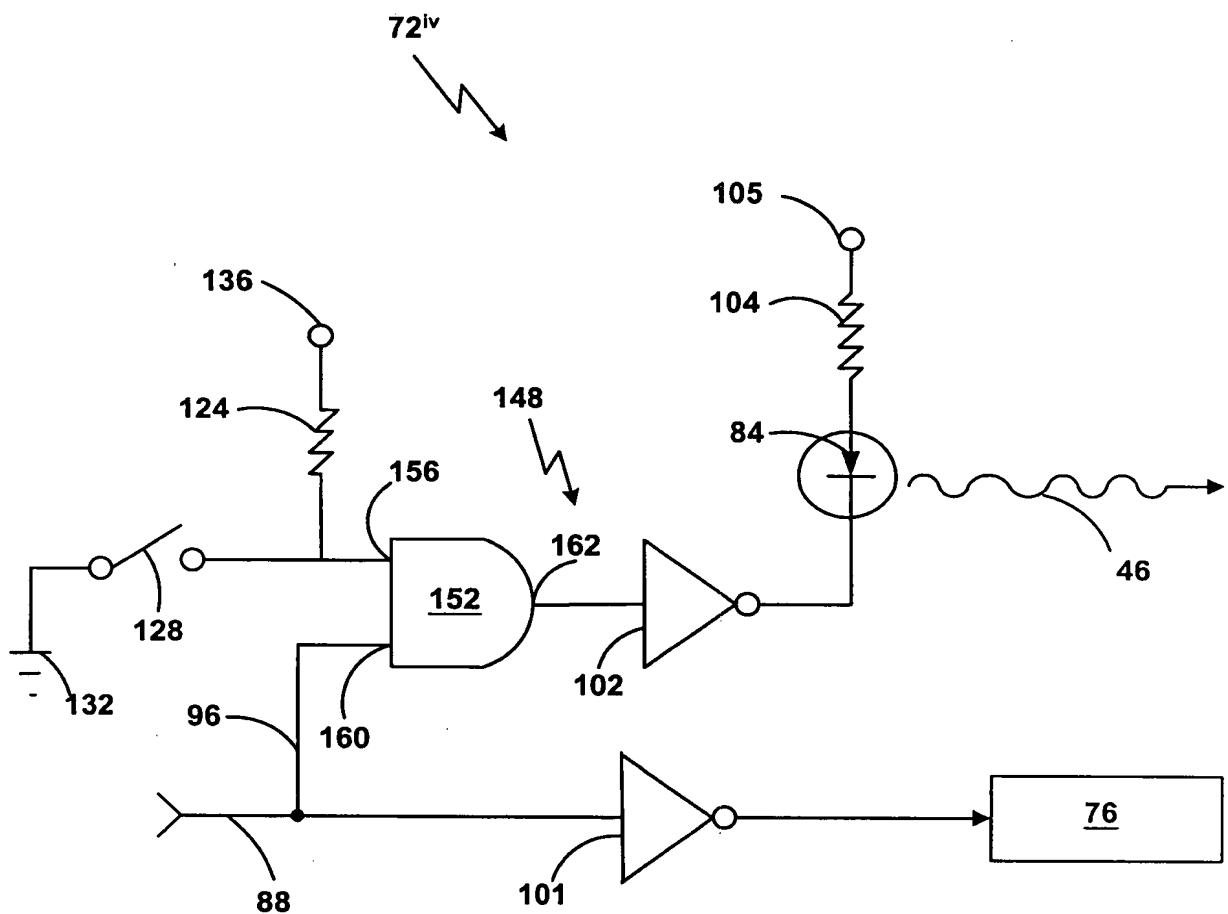


FIG.8

9/20

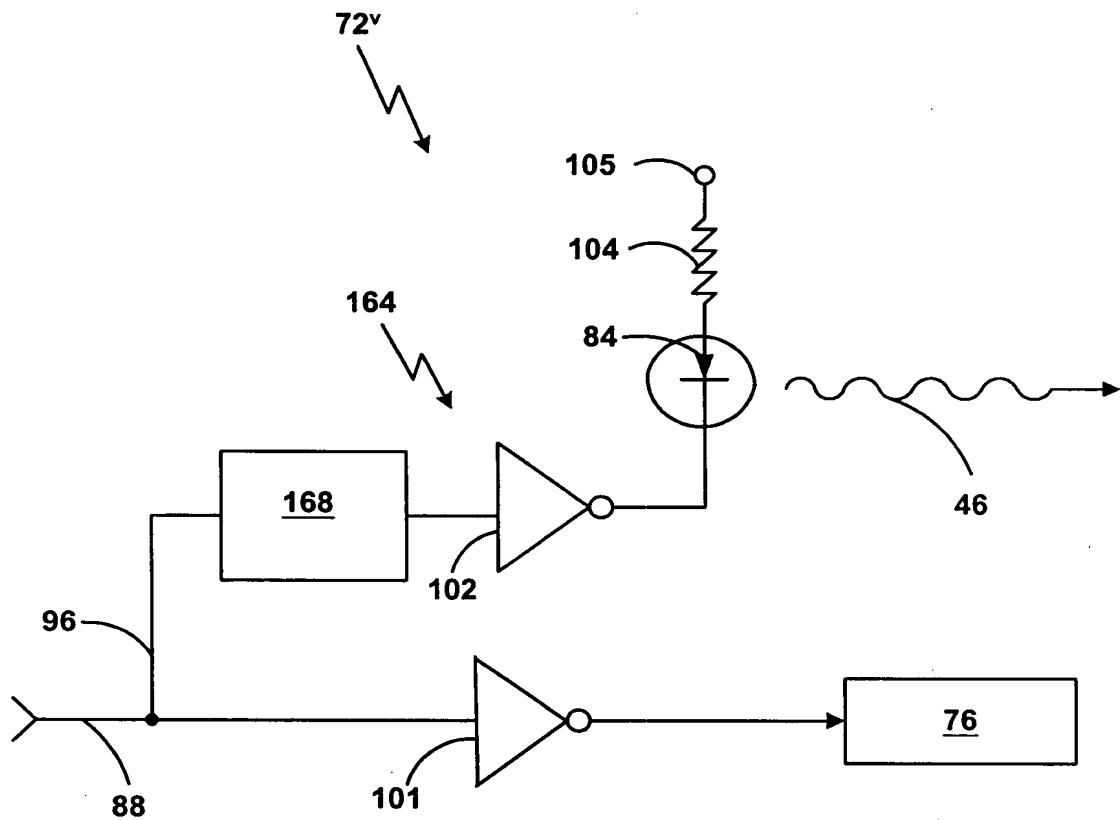


FIG.9

10/20

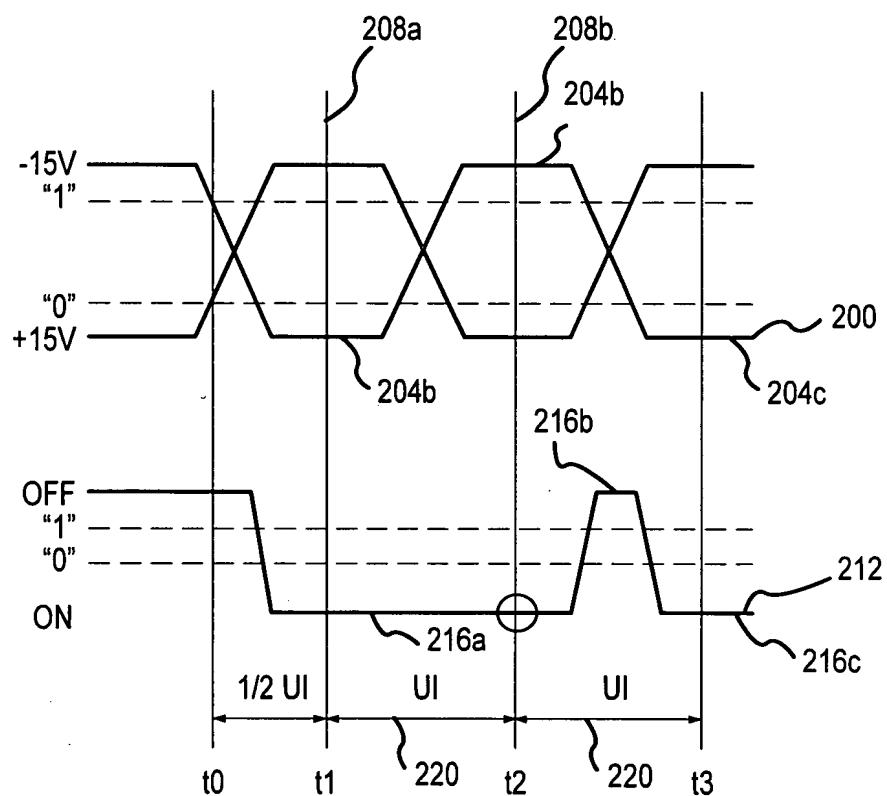


FIG.10

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11/20

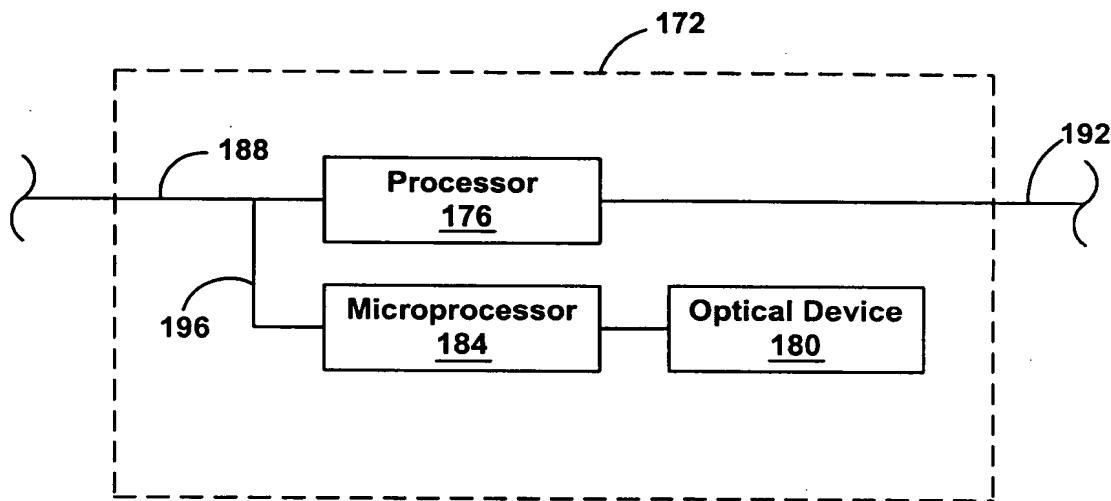


FIG.11

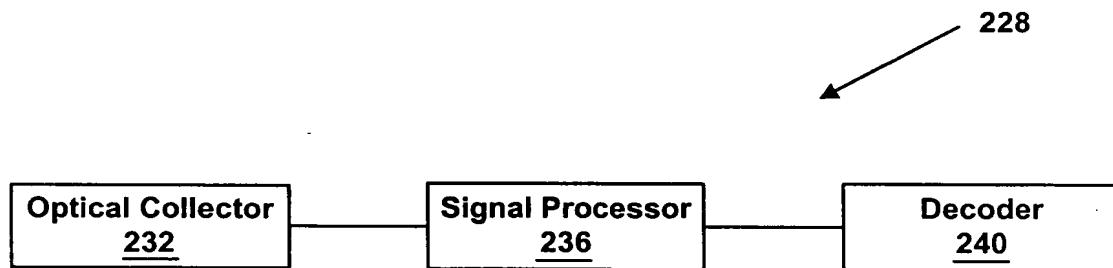


FIG.12

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12/20

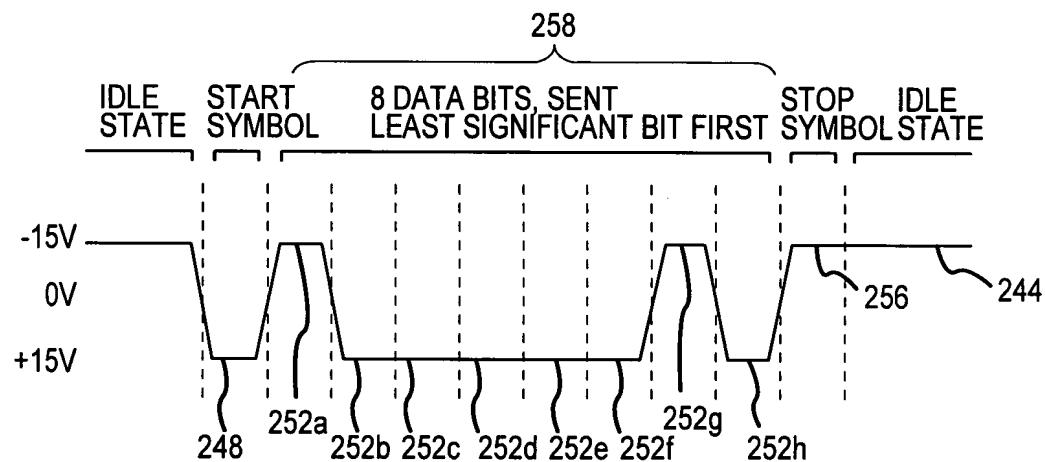


FIG.13

13/20

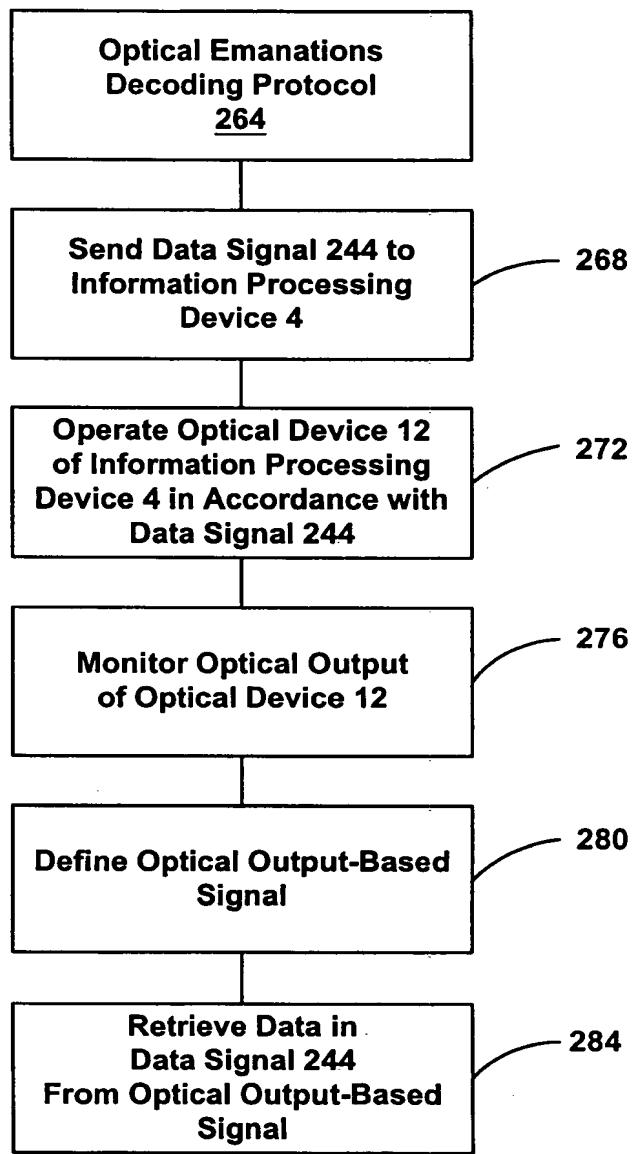


FIG.14A

14/20

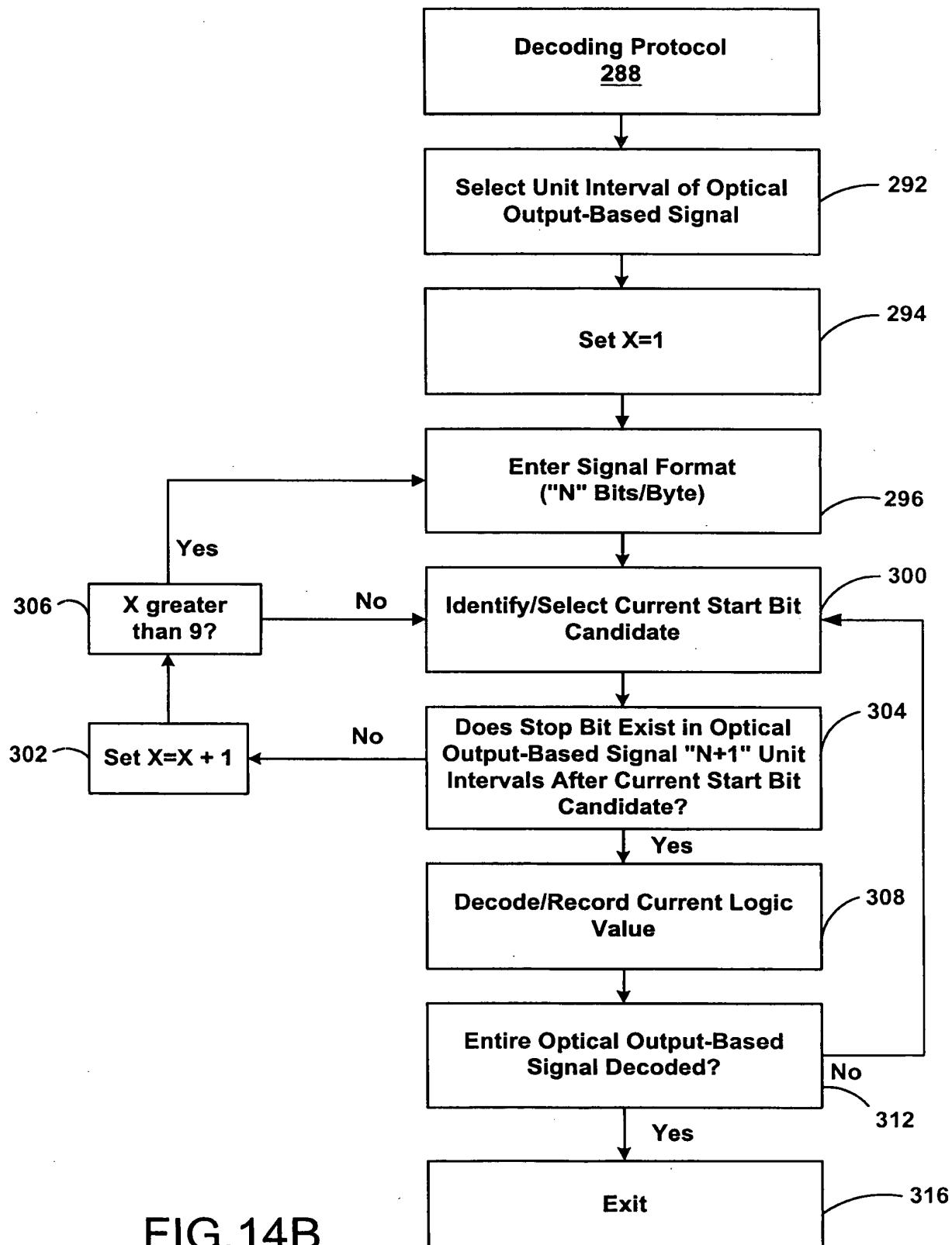


FIG.14B

15/20

Tek Stop: Single Seq 50.0kS/s

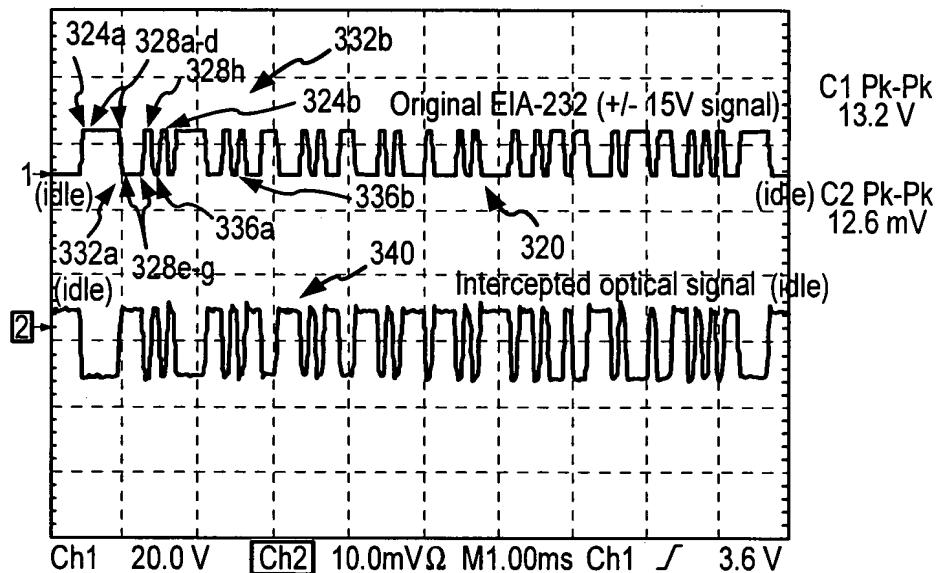


FIG.15A

CHARACTER	IN BINARY	(REVERSE THE BITS)	REVERSED AND FRAMED BY START/STOP BITS
p	01010000	00001010	0000011101 ~ 368a
a	01100001	10000110	0100001101 ~ 368b
s	01110011	11001110	0110011101
s	01110011	11001110	0110011101
w	01110111	11101110	0111011101
o	01101111	11110110	0111101101
r	01110010	01001110	0010011101
d	01100100	00100110	0001001101
<RETURN>	00001011	11010000	0110100001

FIG.15B

16/20

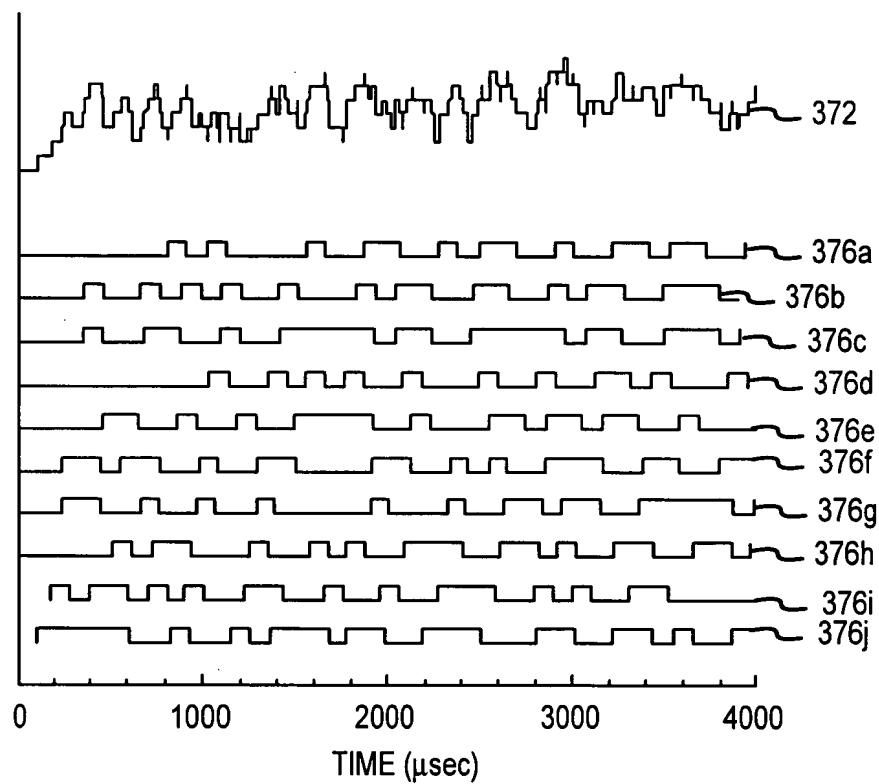


FIG.16

17/20

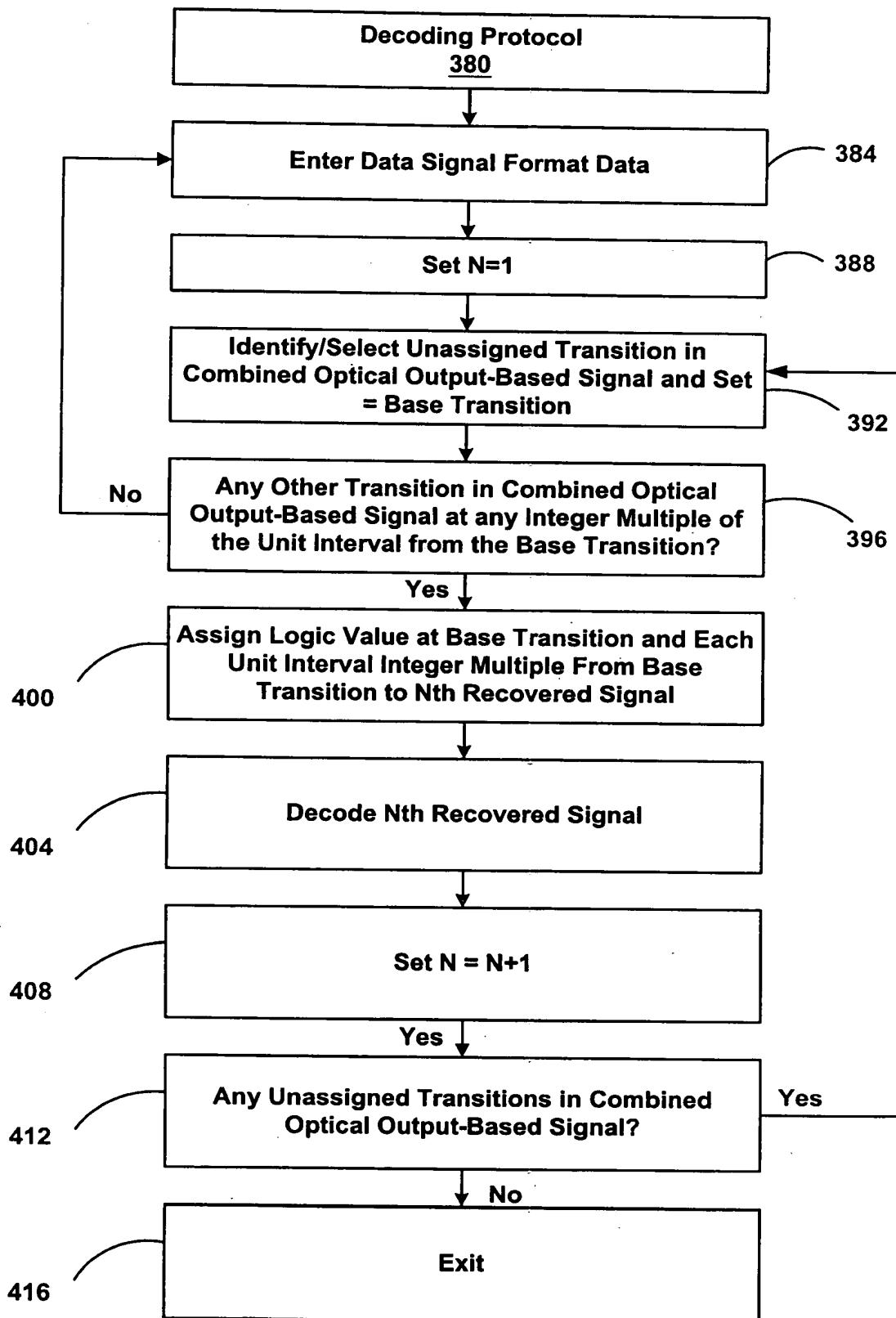


FIG.17

18/20

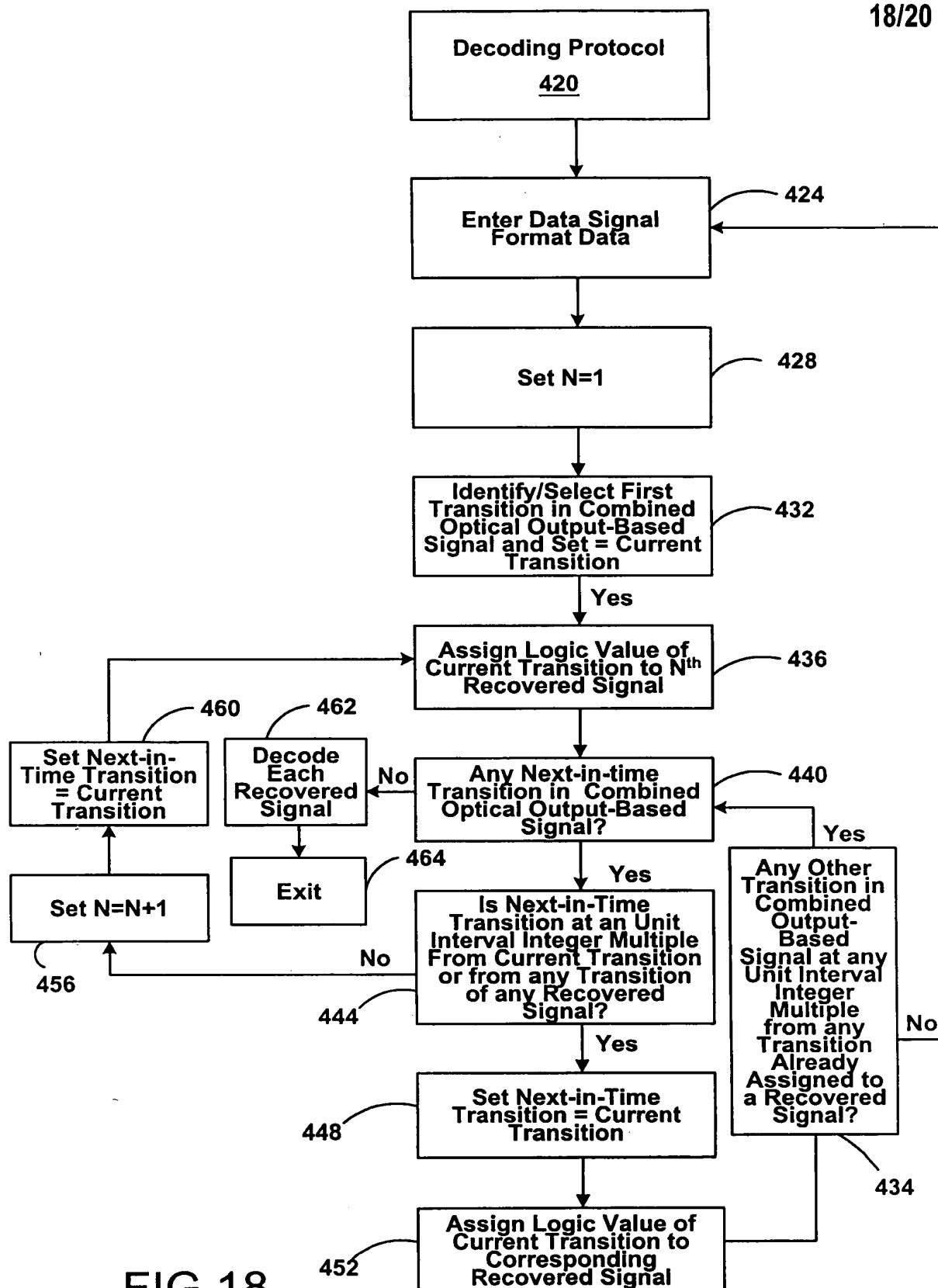


FIG.18

19/20

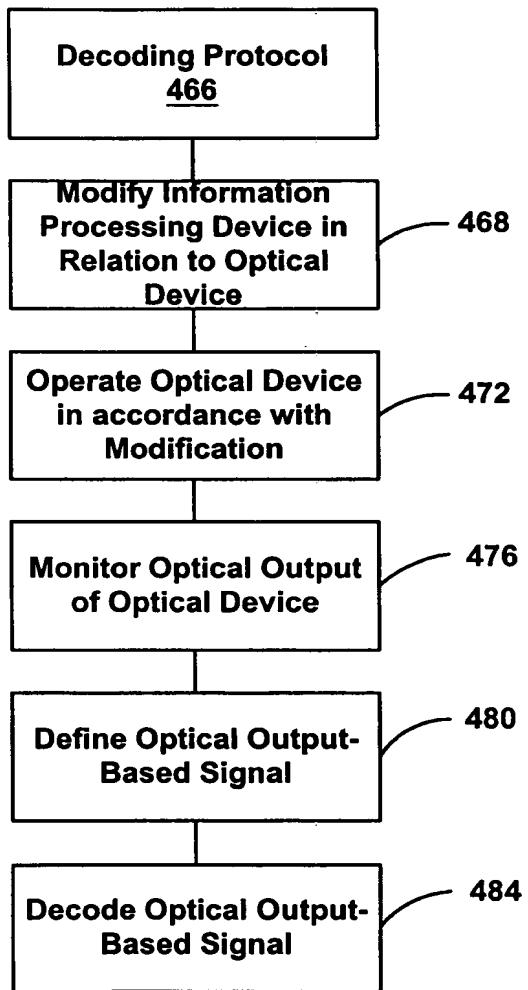


FIG.19

20/20

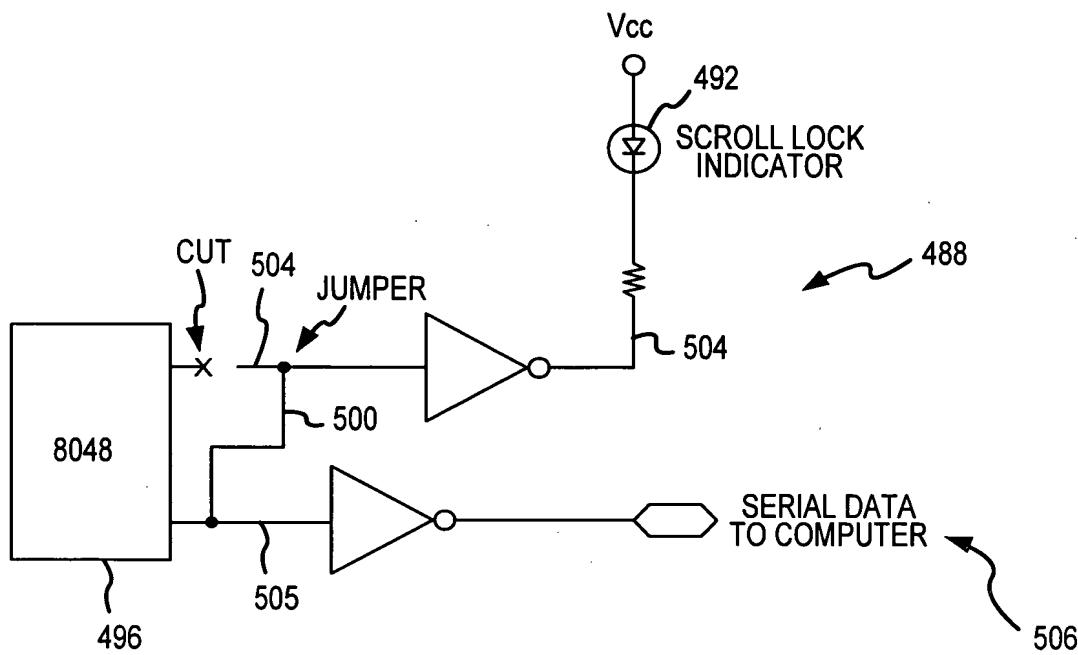


FIG.20

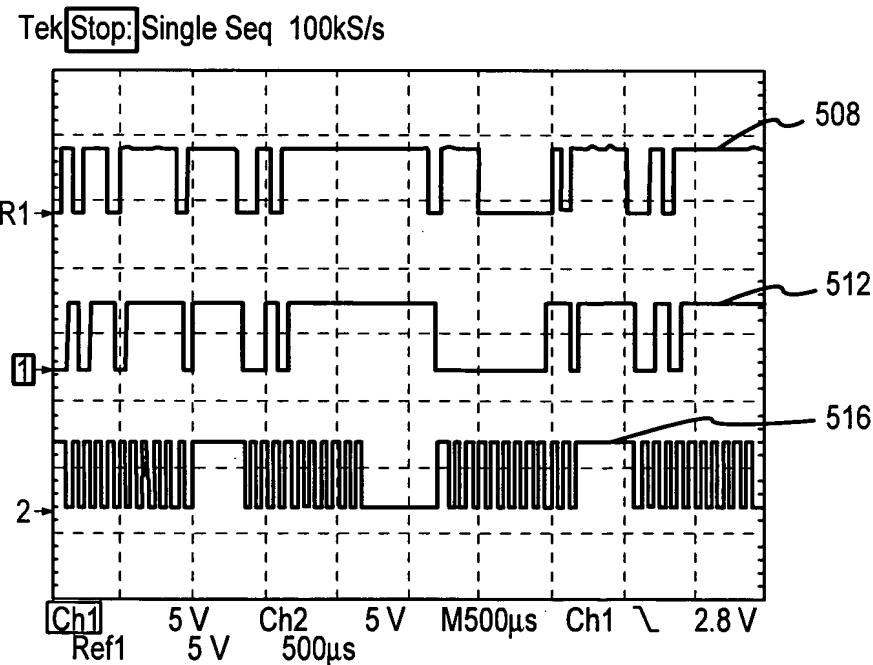


FIG.21